Disjunctions [asks for Logic Table] - Step-by-Step Lesson

a. Make a truth table for the statement:

 $\sim M V N$



Explanation:

If the original is true, the \sim statement is false, and if the original is false, the \sim statement is true. In truth table original of \sim M is true, than \sim M is false.

The symbol " V " signifies inclusive disjunction a V statement is true whenever either (or both) of its component statements is true; it is false only when both of them are false.

We are working with disjunction statements. Disjunctions are true when just one of the other statements are true. Disjunctions are only false when both statements are false.

Let take that to our truth table and we should get:

Μ	Ν	~M	~M V N
Т	Т	F	Т
F	F	Т	Т

