

## Disjunctions [asks for Logic Table] - Guided Lesson Explanation

### Explanation#1

The symbol "**V**" signifies inclusive disjunction a **V** statement is true whenever either (or both) of its component statements is true; it is false only when both of them are false.

K	S	KvS
T	T	T
F	F	F

### Explanation#2

If the original is true, the  $\sim$  statement is false, and if the original is false, the  $\sim$  statement is true. In truth table original of G is true, than  $\sim G$  is false.

The symbol "**V**" signifies inclusive disjunction a **V** statement is true whenever either (or both) of its component statements is true; it is false only when both of them are false.

G	H	$\sim G$	$\sim H$	$\sim G \vee \sim H$
T	T	F	F	F
F	F	T	T	T

### Explanation#3

If the original is true, the  $\sim$  statement is false, and if the original is false, the  $\sim$  statement is true. In truth table original of  $\sim T$  is true, than  $\sim T$  is false.

T	K	$\sim T$	$\sim T \vee K$
T	T	F	T
T	F	F	F

